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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Akhil Garlapati et al.

Title: VOLTAGE REFERENCE GENERATOR CIRCUIT USING LOW-BETA
EFFECT OF A CMOS BIPOLAR TRANSISTOR

Application No.: 10/813,837

Filed: March 31, 2004

Examiner: Rajnikant B. Patel

Group Art Unit: 2838

Atty. Docket No.: 026-0044

Confirmation No.: 6047

January 9, 2006

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COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450**PETITION FOR EXTENSION OF TIME**

This is a petition under the provisions of 37 CFR 1.136(a) to extend the period for filing a response in the above-identified application. The requested extension and appropriate Fee are:

- ☒ One month extension
- ☒ Fee (37 CFR 1.17(a)): \$120
- ☐ Applicant has previously extended the period for filing the response by n/a month(s). Therefore the remaining fee amount under the 37 CFR 1.17(a) to extend the period for a total of n/a month(s) is: n/a.
- ☐ Applicant is a Small Entity. Therefore the amount shown above is reduced by one-half, and the resulting Fee is: n/a.

Payment is by:

- ☐ Check: An enclosed check includes the Fee.
- ☒ Credit Card: Please charge the Fee as directed in the attached Form PTO-2038.
- ☐ Deposit Account: Please charge the Fee to Deposit Account Number: n/a.
- ☒ The Commissioner is hereby authorized to charge any deficiency in fees and any additional fees which may be required, or credit any overpayment, to Deposit Account Number: 50-0631.

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extension of time

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 Nicole Teitler Cave	<u>1/9/07</u> Date

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Respectfully submitted,



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